As an inventor named below or on any ned continuation page, I hereby declare that: My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled STEREOLITHOGRAPHIC METHOD AND APPARATUS FOR FABRICATING STABILIZERS FOR FLIP-CHIP TYPE SEMICONDUCTOR DEVICES AND RESULTING STRUCTURES, the specification of which (check one):

Ø	is attached hereto.	
		as United States application serial no and was amended on
CJ	was filed on	as PCT international application no and was amended under PCT Article 19 on

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.

foreign/PCT application(s):			Priority Clair	ned
(number)	(country)	(day/month/year filed)	Yes	No
(number)	(country)	(day/month/year filed)	Yes	No
to disclose to the U.S. Patent and Trade	application in the manner provided by the first emark Office all information known to me to be between the filing date of such prior application (filing date)	material to patentability as defined in T	itle 37, Code of ling date of this	Federal
(application serial no.)	(filing date)	(status - pending, patented of	or abandoned)	
ereby claim the benefit under Title 35,	United States Code, § 119(e) of any United Sta	tes provisional application(s) listed belo	ow:	
(provisional application no.)	(filing date)			
ereby appoint the following Registered vith:	Practitioners to prosecute this application and to	transact all business in the Patent and	Trademark Offi	ce conn

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

	(1) (A)	<i>i</i>
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DECLARATION FOR PATENT APPLICATION (continuation page)

Invention title: STEREOLITHOGRAPHIC METHOD AND APPARATUS FOR FABRICATING STABILIZERS FOR FLIP-CHIP TYPE SEMICONDUCTOR

Inventor name(s) appearing on first declaration page: Salman Akram

Additional original, first and joint inventor(s):

Full name of second joint inventor: Syed Sajid Ahmad Inventor's signature Syed Sajid Ahmad	Date	3-9-20	
Residence: Boise, Idaho	-		_
Citizenship: U.S.A.			

TOSSAL CONTROL